


850A Series
Audio Broadcast Quality Transformers

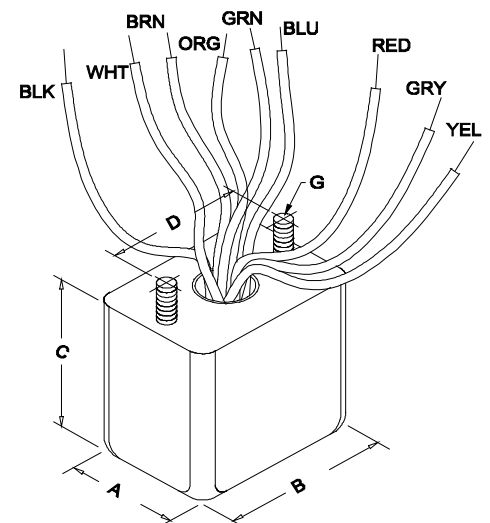
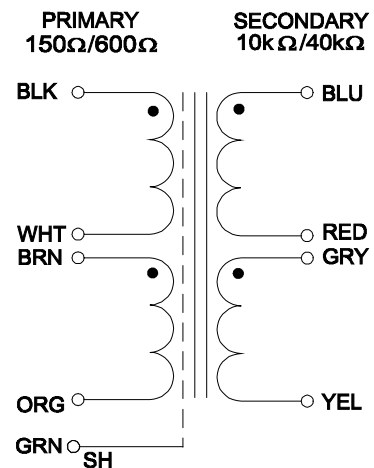
850NA

Features:

- Deep-drawn steel case with tin plated finish, with two convenient 6-32 mounting studs with mounting hardware.
- Sealed case and epoxy-potted transformer for stable characteristics and long life.
- Wide frequency response $\pm 0.5\text{dB}$ max. from 20Hz to 20KHz.
- Maximum power level +15 dBm. with specified characteristics, or higher levels with reduced low frequency performance.
- Distortion is $<1\%$ @ 20 Hz under full power.
- Electrostatic shield between primary & secondary connected to the green lead.
- Humbucking construction
- Balanced split windings on primary & secondary for circuit versatility. Primary may be used as a secondary and vice versa for impedance matching.
- Includes mounting hardware. Shipping weight 0.4 lb. (0.18 kg).
- Lead length: minimum 4"

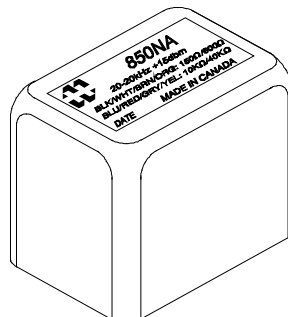
ELECTRICAL SPECIFICATIONS

Characteristics	Typical	
PRI Impedance	150 / 600 Ohms	
SEC Impedance	10K / 40K Ohms	
Output Power	+15dBm (31.623mW)	
DCR BLK – WHT = BRN – ORG	16.9 Ohms $\pm 20\%$	
DCR BLU – RED = GRY – YEL	1258 Ohms $\pm 20\%$	
Dielectric Strength	250V RMS	
PRI Inductance Impedance		
	1V @ 1KHz OC	
BLK&BRN joined – WHT&ORG joined	226mH	5.58K Ohms
BLK – ORG (WHT&BRN joined)	776mH	19.82K Ohms
PRI Leakage Inductance		
	1V @ 1KHz SC	
BLK&BRN joined – WHT&ORG joined	270uH	
BLK – ORG (WHT&BRN joined)	1.08mH	



DIMENSIONAL DETAILS:

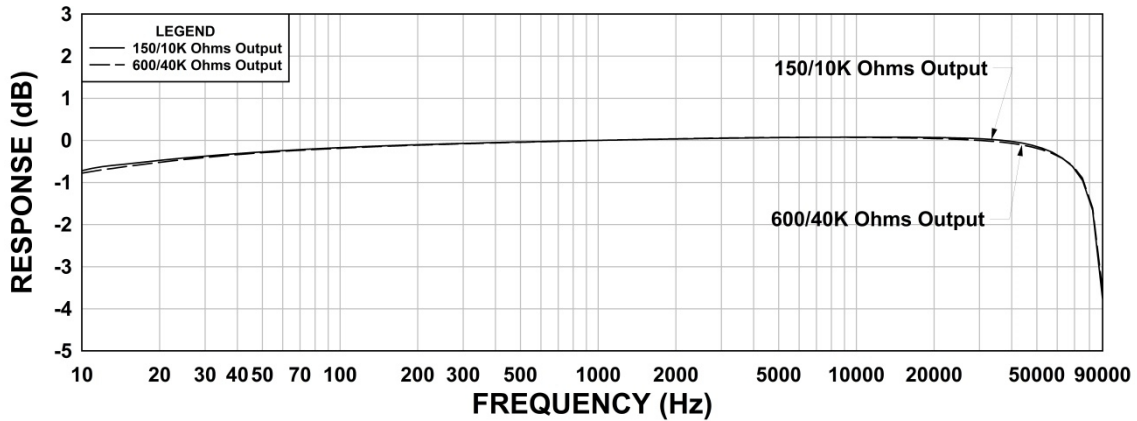
DIMENSIONS:	
A	1.20" ± 0.063
B	1.70" ± 0.063
C	1.65" MAX.
D	1.32" ± 0.063
G	6-32 mounting studs



PERFORMANCE GRAPHS:

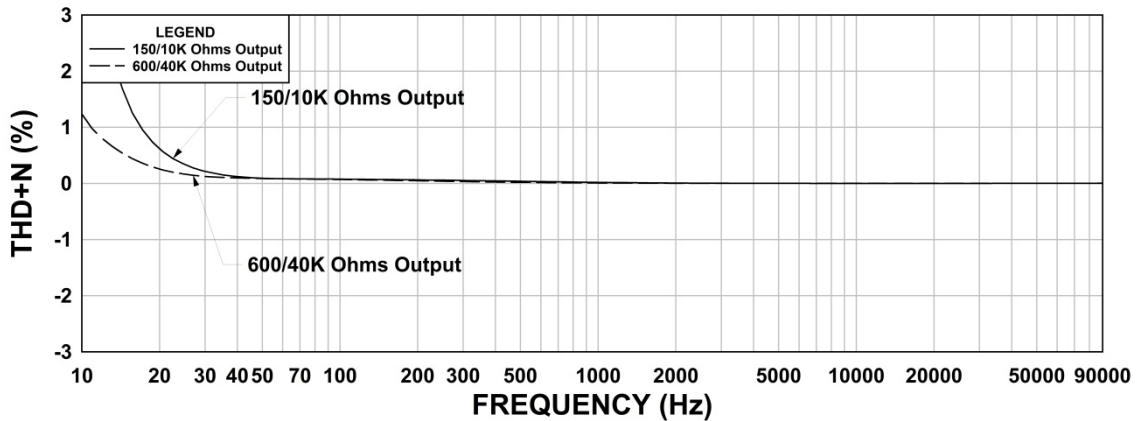
850NA Frequency Response

15dBu @ 1KHz Reference



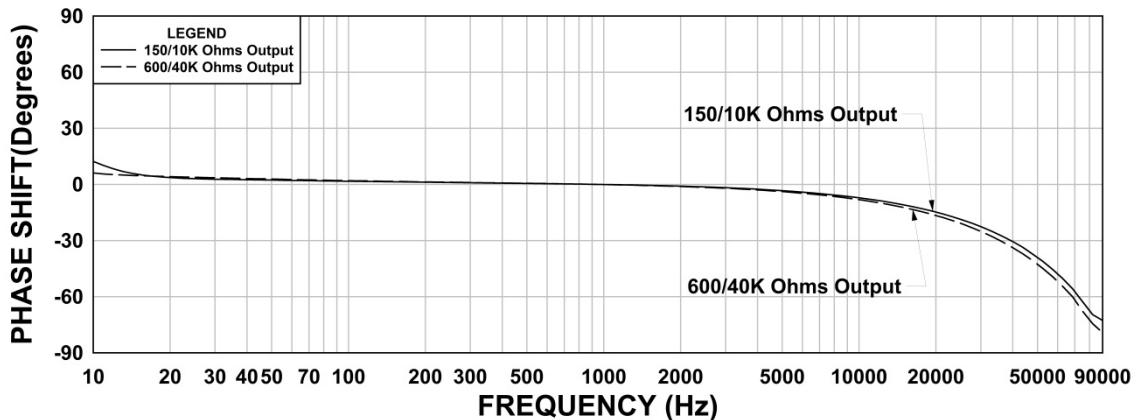
850NA THD+N

15dBu @ 1KHz Reference



850NA Phase Shift

15dBu @ 1KHz Reference

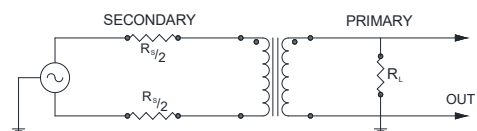


MEASUREMENT INSTRUMENTS

- dScope Series III Audio Analyzer
- Wayne Kerr 3255B with a 3265B Inductance Analyzer
- HP 4192a LF Impedance Analyzer
- Keithley 2010 DVM

**The results are typical and are subject to normal manufacturing and electrical tolerances.

TEST CONDITIONS



Test circuit configured to match test equipment's input and output impedance.